	सी.एस.आई.आर. मद्रासकॉम्प्लेक्स **CSIR MADRAS COMPLEX** (वैज्ञानिक तथाऔद्योगिकअनुसंधान परिषद) (Council of Scientific and Industrial Research) **CSIR Road, CSIR Campus, Taramani, Chennai – 600113.** **अधिसूचना सं./NOTIFICATION NO.EE/02/2023**	Phone: 044-22541271 044-22544540 FAX: 044-22541973

Unit:CEERI

सं./ No.EE/2/2(01)/2023-Rct.   दिन/Dated : 08.08.2023

Applications are invited for engagement of Junior Research Fellow [1 No.] & Project Associate – I [1 No.] for **CSIR-CEERI** Madras Centre, Taramani, Chennai-113 as detailed below:

क्रम सं. Sl. No.	पद पदों की संख्या Positions No. of post (s)	योग्यता Qualification	परियोजना नहीं Project No	वृत्तिका Stipend (per month)	आयु Age As on 29.08.2023	अवधि Duration
1.	**Junior Research Fellow (1 No.)**	**Essential:** B.E./B.Tech degree in Computer Science Engineering or Electronics & Communications Engineering from a recognized university with GATE/NET qualification or similar qualification in any other national level examination conducted by central government departments and institutions. OR M.E./M.Tech degree in Computer Science Engineering or Electronics & Communications Engineering or Optoelectronics or Mechatronics from a recognized University. **Desirable:** Knowledge and experience in optics & photonics, hardware development, applied electronics, analog & digital circuits design. **Remarks:** In case the candidate acquires GATE qualification subsequent to her appointment, revision of emoluments will not be considered	GAP 03142	Rs.31,000/-+ HRA @ 27% per month for 24 months and Rs. 35000/-+ @ 27% per month for last 06 Months.	28 years	Initially for six months and likely to be extended, based on the performance of the candidates till the duration of the Project.

2.	Project Associate- I (1 No.)	**Essential:** B.E./B.Tech degree in Computer Science Engineering or Electronics & Communications Engineering or Mechatronics from a recognized University. **Desirable:** Knowledge and experience in Python programming, hardware development, applied electronics, analog & digital circuits design.	HCP 46	31,000/- + HRA per month	35 Years	Initially for six months and likely to be extended, based on the performance of the candidates till the duration of the Project.

Interested candidates who fulfils the qualification, age, etc. may <mark>WALK-IN FOR INTERVIEW</mark> on **29.08.2023** at **09.30 a.m.** with an application **in the prescribed format duly filled-in** along with **original** and **attested copies** of certificates and testimonials in proof of **Qualification, Age and Community (for SC/ST/OBC), etc.**, and **recent passport size photograph affixed** on the application. No TA/DA will be paid for attending the Walk-In-Interview.
Candidates who come for interview with incomplete particulars / information will not be entertained.

The candidates who have already served in CMC & its participating units or any other Lab./Instt. of CSIR as a Project Assistant / Project Fellow / JRF / SRF / Research Associate etc. for a period of 5 years or more are not eligible for the above engagements. The candidates who have served for a period less than 5 years, upon selection, their tenure will automatically end on completion of five years.

If it is found at any stage of the process or thereafter that the candidates do not fulfill the eligibility criteria, their candidature shall be cancelled without assigning any reason whatsoever. Further, the decision of the Council in any matter relating to this, will be final. Interim enquiries will not be entertained and canvassing in any form will be a disqualification.

Controller of Administration